



## **Worksheet 2 Simplifying Boolean expressions**

### **Answers**

### **Task 1**

1. X, Y and Z are Boolean variables which can be either TRUE or FALSE, represented by 1 and 0.

Complete the following “rules” of Boolean algebra:

#### **General rules**

1.  $X \wedge 0 = 0$

2.  $X \wedge 1 = X$

3.  $X \wedge X = X$

4.  $X \neg X = 0$

5.  $X \vee 0 = X$

6.  $X \vee 1 = 1$

7.  $X \vee X = X$

8.  $X \vee \neg X = 1$

9.  $\neg X = \neg X$

#### **Commutative rule**

10.  $X \wedge Y = Y \wedge X$

11.  $X \vee Y = Y \vee X$

#### **Associative rule**

12.  $X \wedge (Y \wedge Z) = (X \wedge Y) \wedge Z$

13.  $X \vee (Y \vee Z) = (X \vee Y) \vee Z$

#### **Distributive rule**

14.  $X \wedge (Y \vee Z) = (X \wedge Y) \vee (X \wedge Z)$

15.  $(X \vee Y) \wedge (W \vee Z) = (X \wedge W) \vee (X \wedge Z) \vee (Y \wedge W) \vee (Y \wedge Z)$

#### **Absorption rules**

16.  $X \vee (X \wedge Y) = X$

17.  $X \wedge (X \vee Y) = X$



2. Write down de Morgan's first and second laws:

$$\neg(A \vee B) = \neg A \wedge \neg B$$

$$\neg(A \wedge B) = \neg A \vee \neg B$$

3. Use de Morgan's Laws and the rules of Boolean algebra to simplify the following expressions, stating which rule you use at each step.

(a)  $X \wedge Y \vee X \wedge (Y \vee Z)$

$$= (X \wedge Y) \vee (X \wedge Y) \vee (X \wedge Z)$$

Distributive Rule

$$= (X \wedge Y) \vee (X \wedge Z)$$

Rule 7

(b)  $(X \vee Y) \wedge (\neg X \vee \neg Y)$

$$= (\cancel{X}X)(XY)(\cancel{Y}X)(YY)$$

Distributive Rule

$$= 0(XY)(YX)0$$

Rule 4

$$= XY YX$$

Rule 5

(c)  $X(\overline{XY})$

$$= X\overline{X}Y$$

(de Morgan's Law)

$$\hookrightarrow 1Y \quad \text{Rule 8}$$

$$\hookrightarrow 1$$

Rule 6

(d)  $(X \vee Y)(X \vee Z)$

$$= (XX)(XZ)(YX)(YZ)$$

Distributive Law

$$= X(XZ)(YX)(YZ)$$

Rule 3

$$= X(YX)(YZ)$$

Absorption rule

$$= X YZ$$

Absorption rule

Note that "multiplying out" the brackets  $(X \vee Y)(X \vee Z)$  is very much easier in the alternative notation using . for AND and + for OR, giving

$$(X + Y).(X + Z) = (X.X + X.Z + Y.X + Y.Z) \quad \text{as in maths notation}$$



4. Complete the truth table to show that  $A \vee (A \wedge B) = A \vee B$

A	B	$\neg A$	$\neg A \wedge B$	$A \vee \neg A \wedge B$	$A \vee B$
0	0	1	0	0	0
0	1	1	1	1	1
1	0	0	0	1	1
1	1	0	0	1	1

## Task 2

5. Simplify the expression  $A \vee B \vee A \wedge (B \vee C)$

$$A \vee B \vee (B \vee C)$$

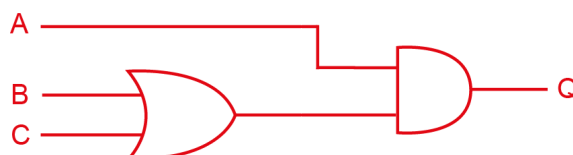
commutative rule

$$A \vee (A \vee B) \vee (B \vee C)$$

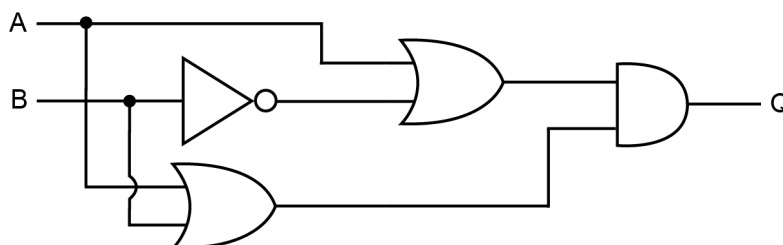
$$A \vee (B \vee C)$$

absorption rule

Draw a logic circuit representing the simplified expression, using only 2 gates.



6. (a) Write the Boolean expression representing the logic circuit below.



$$(A \vee B) \wedge (A \vee B)$$



(b) Complete the truth table to prove the Absorption rules:

$$X \vee (X \wedge Y) = X$$

$$X \wedge (X \vee Y) = X$$

X	Y	$(X \wedge Y)$	$(X \vee Y)$	$X \vee (X \wedge Y)$	$X \wedge (X \vee Y)$
0	0	0	0	0	0
0	1	0	1	0	0
1	0	0	1	1	1
1	1	1	1	1	1

(c) Simplify the expression.

$$A \vee (A \wedge B) \vee (A \wedge \neg B) \vee (B \wedge \neg B) \quad (\text{Tip: Use the Absorption rule})$$

In the alternative notation, (AND) is the same as  $\cdot$  and  $\vee$  (OR) is the same as  $+$ .

As in Maths, given for example  $A \cdot B + A \cdot C$ , you can insert brackets to get  $(A \cdot B) + (A \cdot C)$ , but you cannot insert brackets to the terms either side of the  $+$  to give  $A \cdot (B + A) \cdot C$

Start by bracketing the expressions involving AND on each side of the OR operators

$$= (A \vee A) \vee (A \wedge B) \vee (A \wedge \neg B) \vee (B \wedge \neg B)$$

Next, note that  $(A \vee A) = A$ , (Rule 3) and  $(B \wedge \neg B) = 0$  (Rule 4)

So we get

$$A \vee (A \wedge B) \vee (A \wedge \neg B) \vee 0$$

$$A \vee (A \wedge B) \vee (A \wedge \neg B)$$

Rule 5

$$= A \vee (A \wedge \neg B)$$

Using the Absorption rule

$$= A$$

Using the Absorption rule again

(d) With reference to the above example, explain why de Morgan's Laws and the rules of Boolean algebra have a huge commercial significance in the manufacture of computers.

In the above example, the output Q has been reduced to a single input A, saving on the manufacture of four unnecessary logic gates. This saves both manufacturing costs and processing time.